



COEP Technological University Pune (COEP Tech)

A Unitary Public University of Government of Maharashtra
(Formerly College of Engineering Pune)

Wellesley Road, Shivajinagar, Pune-411005, Maharashtra, India

Tel: 020-25507000/09

Website: www.coeptech.ac.in

COEP_TECH: Registrar Office/ANRF-PAIR Projects/Recruitment/ December 2025/

Date: 22/07/2026

Rolling Advertisement for the project positions under the ANRF-PAIR project

IIT-B COEP-TU Hub-Spoke model (RP/2025/COEPTech/SSM_others/34 dt. 09/12/2025)

Established in the year 1854, College of Engineering Pune (COEP) is the third oldest engineering institution in Asia, having glorious legacy of 170 years. COEP was bestowed with the status of “Unitary Technological Public University” in June 2022. It is now functional as COEP Technological University (COEP Tech). To reinforce the teaching, learning and research excellence at COEP Technological University, applications are invited for temporary Project Positions under the ANRF-PAIR Hub-Spoke model in collaboration with IIT Bombay. For more information about the project visit <https://anrfonline.in/ANRF/PAIR?HomePage=New>

This is a rolling advertisement. Applications will be processed periodically by 20th of every month.

Summary of the Project titles, Research Positions, Qualification, Experience and Emoluments:

Project UID No.	Project Title	Post Code (No. of Post)	Essential Qualification	Desirable Experience	Job Description & Responsibilities	Monthly Emoluments as per ANRF norms
01	Development of a Switched Reluctance Motor and Permanent Magnet Assisted Synchronous Reluctance Motor for Electric Tractor	01/RA-1 (No. of Post: 1)	Ph. D. completed/ nearing completion in Electrical / Electrical and Electronics / Instrumentation / Electronics engineering with quality publications OR	• Proficiency in embedded systems and familiarity with various development platforms is essential. • Significant experience in dynamic modelling, simulation, and analysis of	• Design, prototype and execute experiments to validate PMA synchronous Reluctance motor and Switched reluctance motor. • Perform thermal FEA studies and suggest suitable	Initially for 1 year (Extendable up to 4 years subject to satisfactory performance)



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			3 years of research, teaching and design and development experience after Master's degree (M.E./MTech/ M.S.) in Electrical / Electronics / Instrumentation engineering with at least one research paper in SCI journal. Preference will be given to candidate with valid GATE score.	power electronics and electric machines/electromechanical systems. • Proficiency in simulation tools such as Ansys Maxwell and MATLAB/Simulink • Expertise in Electromagnetic FEA tools such as Ansys Maxwell, Motor-CAD, or Flux • Strong understanding of Power Electronics (e.g., inverters / converters, gate drivers, IGBTs, MOSFETs). • Expertise in Motor Control Theory, including Field-Oriented Control (FOC) and PID tuning.	cooling for the motor. • Experience with control systems modelling and simulation tools (MATLAB/Simulink or equivalent). • Proven ability to troubleshoot and resolve complex hardware and software integration issues.	and fund availability). Year 1 to 5: ₹ 58,000 /- + 30% HRA
02	GaN based DC-DC converters for next - generation EV power electronic subsystems: Research Innovations and Educational Resources	02/JRF-1 (No. of Post: 1)	M.Sc. in in Physics / Electronics OR Graduate/Post Graduate Degree in Engineering (B.E./B.Tech/M.E./M.Tech) in Electrical / Electronic / Instrumentation / Electronics and Telecommunication Engineering and equivalent	Candidate with relevant experience in DC-DC converters, power electronics and embedded systems	--	Monthly Emoluments: ₹ 37,000/- + 30%HRA



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09	Deepfake Detection using Deep Learning	09/NPDF-1 (No. of Post: 1)	Eligibility: • The applicant should be an Indian citizen. • The applicant must have obtained Ph.D degree from a recognized University. Those who have submitted their PhD/MD/MS thesis and are awaiting award of the degree are also eligible to apply. However, such candidates, if selected, will be offered lower fellowship amount till they qualify the eligible degree. • The upper age limit for the fellowship is 35 years at the time of the submission of application; age will be calculated by taking the date of closure of the respective call. Age relaxation of 5 (five) years will be given to candidates belonging to SC/ST/OBC/Physically Challenged & Women candidates. • NPDF can be availed only once by a candidate in his/her career. Qualification: Ph.D. in Computer Science &	• Proven publications in reputed journals and conferences in the field of image/video processing, cybersecurity, deep fake detection and other relevant research work. • Basic understanding of image/video processing, database systems	• Proven publications in reputed journals and conferences in the field of image/video processing, cybersecurity, deep fake detection and other relevant research work. • Basic understanding of image/video processing, database systems • Actively contribute to the research on Technologies for Digital Privacy and Security • Design and develop security solutions to detect the deepfakes in textual, image, video, audio formats • Publish research contributions in reputed journals and conferences	₹ 80,000/- + ₹ 24000 HRA (30%) = ₹ 104000/- per month
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			Engineering/ Computer Engineering/ IT/Electronics and Telecommunication			
		09/SRF-1 (No. of Post: 1)	• M.Sc. in Computer Science / MCA with proven research publication in the field of Information & Cyber Security OR • Graduate/Post Graduate Degree in Engineering/ Technology (B.E./B.Tech/M.E./M.Tech) in Computer Engineering / Information Technology / Computer Science & Engineering / Electronics / Electronics & Telecommunication. OR • Registered for PhD in Computer Science or relevant branches (mentioned above) and nearing completion with publication in SCI journals in the domain of Information & Cyber security	• GATE Qualified candidates in Computer Science & Engineering OR Data Science & Artificial Intelligence AND having minimum experience as JRF will be preferred • Hands-on skill of python programming, AI framework such as TensorFlow/ PyTorch • Strong theoretical knowledge of Artificial Intelligence, Machine Learning, & DL Data Analytics, Video Analytics, Mathematical fundamentals pertaining to AI • Knowledge and experience in developing Cybersecurity solutions • First-hand experience Nvidia Cards, deploying AI models for experimentation • Scientific writing skills (demonstrated by publications, if any).	• Survey Deepfake Detection (multimodal) and identify gaps. • Propose novel solutions for identified cyber security issues. • Deploy and experimentally validate DL models on GPU systems. • Publish findings in high-impact journals and present at conferences.	₹ 42,000/- + 30%HRA Initially for a period of 1 year further extendable up to additional 4 years subject to satisfactory performance and availability of funds.



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Total Number of Post	04				
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Candidates must apply in the prescribed **Application form** available on COEP Technological University website "www.coeptech.ac.in" under "Careers" tab.

Other Terms and Conditions

1. All positions are purely temporary in nature and are co-terminus with the ANRF-PAIR Project (2025–2030) or until completion of the project, or as per availability of funds.
2. These appointments do not confer any right to regular or permanent employment at COEP Technological University, Pune.
3. The post shall be treated as *non-vacational*, as applicable, in accordance with the norms of COEP Technological University and ANRF.
4. The appointment order shall be issued only after due vetting and approval by the competent legal authority of COEP Technological University.
5. Selected candidates shall be required to submit an affidavit at the time of joining, affirming acceptance of all terms and conditions of the project appointment.
6. COEP Technological University reserves the right to modify, restrict, or cancel the recruitment process, any application, or any post without assigning any reason thereof.
7. Applications submitted without required testimonials or with incomplete information are liable to be rejected.
8. No Travelling Allowance (TA) or Dearness Allowance (DA) shall be paid for attending the interview.
9. Shortlisted candidates will be notified via their registered email ID, and the final list of candidates called for interview will also be displayed on the University website.
10. Applications in the prescribed format, along with photocopies of relevant testimonials be submitted in an envelope superscribed as:
"APPLICATION FOR RESEARCH POSITION (POST CODE:) UNDER ANRF-PAIR Programme"



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For Example: *"APPLICATION FOR RESEARCH POSITION (POST CODE:01/RA-1) UNDER ANRF-PAIR Programme"*

11. Candidates applying for multiple posts should send separate application for each post they have applied for.
12. **Submission of Application** The completed application form must reach through **Registered Post / Speed Post / Courier** latest by **20th of the month** till **5:00 pm** addressed to:

To,
The Registrar,
COEP Technological University,
Wellesley Road, Shivajinagar,
Pune – 411 005,
Maharashtra, INDIA.

13. Clarifications

For any clarifications or queries related to the recruitment, candidates may contact via email at mohitess.mech@coeptech.ac.in.

Place: Pune 411005
Date: 22/07/2026

Sd/-
Registrar
COEP Technological University Pune