



COEP TECHNOLOGICAL UNIVERSITY

A Unitary Technological University of Government of Maharashtra

(Formerly College of Engineering Pune (COEP))

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COEP/E&E Cell/TT RS/2026/ 654

Date: 22/04/2026

Corrigendum

Subject: Corrigendum: Rescheduling of Final Year B.Tech E&TC ESE (April–May 2026)

With reference to the notice Ref: COEP Tech/Dean Office/2026/613 dated 22nd April 2026 regarding the rescheduling of End Semester Examinations (ESE) for Final Year B.Tech Electronics and Telecommunication Engineering (Even Semester).

The examinations for the Courses Analog CMOS Design and Embedded Software and RTOS have been rescheduled as detailed below:

Sr. No.	Branch Name	Course Name	Previously Scheduled Day / Date / Time	Rescheduled Day / Date / Time
1	Electronics and Telecommunication Engineering	Analog CMOS Design	Thursday, 23/04/2026, 02.30 PM to 05:30 PM	Wednesday, 29/04/2026, 02:30 PM to 05:30 PM
2		Embedded Software and RTOS		

Venue: E&TC (NC 22,23)

All students concerned are advised to take note of the above change and appear for the examination accordingly.


22/4/26

Director,

Board of Examinations and Evaluation
COEP Technological University Pune.