

Department of Electronics and Telecommunication Engineering

COEP Technological University

(A Unitary Public University of Government of Maharashtra)

Time Table

Class : F. Y. M. Tech - VLSI Design

With Effect From : 20.08.2025

Academic Year : 2025 - 2026

Term : I (First)

Day TIME	9.30 am To 10.30 am	10.30 am To 11.30 am	11.30 am To 12.30 pm	12.30 pm To 1.30 pm	1.30 pm To 2.30 pm	2.30 pm To 3.30 pm	3.30 pm To 4.30 pm	4.30 pm To 5.30 pm	5.30 pm To 6.30 pm
MON		Digital IC Lab (SFR) Internet Lab			PSE-I: ML (Adjunct) ETSH - 201	Digital IC (SFR) ETSH - 201	RM&IPR (MOOC)		
TUE		RTL (VVI) ETSH - 201	RTL (VVI) ETSH - 201		Digital IC (SFR) ETSH - 201	Digital IC (SFR) ETSH - 201	RM&IPR (MOOC)		
WED		RTL (VVI) ETSH - 201	RTL Lab Tut (VVI) ETSH - 201		PSE-I: ML (Adjunct) ETSH - 201	GFRT (Adjunct) ETSH - 201	ETC (MOOC)	Expert Lecture	
THU		IC Fab (PBP) ETSH - 201	IC Fab (PBP) ETSH - 201		GFRT (Adjunct) ETSH - 201	RTL Lab (VVI) VLSi Lab			
FRI		GFRT (Adjunct) ETSH - 201	IC Fab Tut (PBP) ETSH - 201		PSE-I: ML (Adjunct) ETSH - 201	Seminar (VVI) ETSH - 201			

RTL Simulation and Synthesis (RTL) (Th + Lab): Dr. Mrs. V. V. Ingale, **IC Fabrication Techniques (IC Fab)(Th + Tut):** Dr. P. B. Pawar

Digital IC Design (Digital IC)(Th + Lab): Dr. Mrs. Saiyma F. Raza, **Graph, Field and Ring Theory for Security and Physical design (GFRT):** Adjunct

Seminar (Lab): Dr. Mrs. V. V. Ingale, **Program Specific Elective – I: Machine Learning (ML):** Adjunct

In-charge, Timetable Committee,
Department of Electronics & Telecommunication Engineering