



COEP Technological University

(COEP Tech)

A Unitary Public University of Government of Maharashtra

(Formerly College of Engineering Pune)

Department of Electronics and Telecommunication

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Enquiry Letter

Sealed Quotation are invited by the COEP Technological University from reputed manufactures/ suppliers for the supply of the following item.

Enquiry Number & Date	COEP/E&TC/Enq/Institute Equipment /Kits for Basic Electronics Laboratory /2025-26/
Enquiry Date	22/08/2025
Material Description	Kits for Basic Electronics Laboratory
Location: -	Department of E&TC, COEP Technological University
Quotation Submission Date@ Time	01/09/2025 @4 P.M.
Quotation Submission Place	Department of E&TC, COEP Technological University, Wellesley Road, Shivajinagar, Pune-411005
Quotation Opening Place	Office of Department of Electronics and Telecommunication Engg..

Terms & Conditions: -

1. Fax and Email quotation are not acceptable.
2. The taxes, insurance, freight, packing and forwarding charges if any be quoted in Indian Rupees separately.
3. Validity: Quotation Validity at least 90 days from the due date.
4. Quotations shall be sent in sealed envelopes clearly marked Quotation for Supply of, Enquiry Number, Enquiry date and Enquiry due date addressed to The Head, Department of Electronics & Telecommunication Engineering, COEP Technological University Pune-411 005.
5. 100% payment will be paid after satisfactory delivery, installation and commissioning/work.
6. Please specify the make and model of the item.
7. Quotation(s) received after last date of Quotation submission will be rejected.
8. Delivery/Work Period and Terms Conditions should be mentioned clearly.
9. Delivery/Work: The penalty conditions are applicable for the late delivery as per government norms.
10. Optional items should be quoted in separate sheet otherwise your quote will be rejected
11. Supply/Work and Installation: - Vendor shall be responsible for successful installation, commissioning and testing of the supplied items at Department of Department of Electronics & Telecommunication Engineering, COEP Technological University Pune-411 005.. Any defective component/device will be replaced by vendor at his cost.
12. The Vice Chancellor of COEP Technological University Pune reserves right to reject any one or all the quotation(s) without assigning any reasons there for.

Sr. No	Product description with key features and specifications	Qty	Price/ Unit in Rs including all taxes	Total Amount in Rs including all taxes
01	Clipper and Clamper Circuits Technical Specification: Operating DC supply : $\pm 5V$, 200mA Circuit type : Diodes (2N4007) Circuit configuration : Single Ended and Double ended Clipper Circuits Positive and Negative Clamper Circuits Experiments : Study of Single Ended and Double ended Clipper Circuits Study of Positive and Negative Clamper Circuits Interconnections : 2 mm sockets Study points : Test points to study signals at various stages of circuit Other features : on-board Schematic diagram Flexibility of making circuit connections Manual : Operating manual and Experiments along with detailed theory	04		
02	Voltage Multiplier Circuits Technical Specification: Operating DC supply : $\pm 12V$, 200mA Circuit type : Diodes (2N4007) Circuit configuration : Single Ended and Double ended Clipper Circuits Positive and Negative Clamper Circuits Experiments : Study of Voltage Multiplier Circuits To observe the effect of Variation of Frequency for Voltage Multiplier To observe the effect of Load Regulation for Voltage Multiplier Interconnections : 2 mm sockets Study points : Test points to study signals at various stages of circuit Other features : on-board Schematic diagram Flexibility of making circuit connections Manual : Operating manual and Experiments along with detailed theory	04		

03	Half wave and Full wave Rectifier	04		
04	Technical Specification:			
05	<p>Operating AC supply : 18V - 0V -18V</p> <p>Circuit type : Rectifier Circuit using Diodes (2N4007)</p> <p>Circuit configuration : Half wave and Center Tapped Full wave Rectifier circuits</p> <p>Experiments : Study of Half Wave Rectifier Study of Center Tapped Full Wave Rectifier Study of Rectifier Output with Filter and without Filter</p> <p>Interconnections : 2 mm sockets</p> <p>Study points : Test points to study signals at various stages of circuit</p> <p>Other features: on-board Schematic diagram</p> <p>Flexibility of making circuit connections</p> <p>Manual : Operating manual and Experiments along with detailed theory</p>			
06	BJT Characteristics and its h-Parameters	04		
	<p>Technical Specification:</p> <p>Operating power supply : +12V, +5V DC, 200mA</p> <p>Circuit type : NPN Transistor BC 547</p> <p>Circuit configuration : CE Input output Characteristics of BJT and its h-Parameter</p> <p>Experiments : Study of CE Input output Characteristics of BJT</p> <p>Study of h-Parameter of BJT</p> <p>Interconnections : 2 mm sockets</p> <p>Study points : Test points to study signals at various stages of circuit</p> <p>Other features: on-board Schematic diagram</p> <p>Flexibility of making circuit connections</p> <p>Manual : Operating manual and Experiments along with detailed theory</p>			

07	<p>Single Stage BJT CE Amplifier</p> <p>Technical Specification:</p> <p>Operating power supply : +12V DC, 200mA</p> <p>Circuit type : using NPN Transistor</p> <p>Circuit configuration : Single Stage BJT CE Amplifier</p> <p>Experiments : Basic operation of CE Transistor Amplifier circuit</p> <p>Study of Single Stage BJT CE Amplifier and evaluation of :</p> <ol style="list-style-type: none"> Operating Point of the Amplifier Voltage gain of the Amplifier A_V Input and Output Impedance of the Amplifier Current Gain of the Amplifier <p>Interconnections : 2 mm sockets</p> <p>Study points : Test points to study signals at various stages of circuit</p> <p>Other features: on-board Schematic diagram</p> <p>Flexibility of making circuit connections</p> <p>Manual : Operating manual and Experiments along with detailed theory</p>	04		
08	<p>Nvis 6550 Understanding and Experimentation with Digital ICs</p> <p>It should have following features:</p> <ul style="list-style-type: none"> • Illustration of Combinational and Sequential circuits • ZIF Socket provided for easy connections • Compact size • Simultaneous use of multiple ICs <p>It should have following Technical Specification:</p> <p>Mains Supply : 90V - 250V, 50Hz</p> <p>Fixed DC Power Supply : +12V, -12V, +5V, -5V</p> <p>Fixed TTL Generator : 1Hz, 10Hz, 100Hz, 1kHz, 10kHz and 100kHz</p> <p>Pulse Generator : 5V</p> <p>ZIF Socket : 20 Pins (6 Nos.) 40 Pins (1 No.)</p> <p>Input Section : 12 toggle switches</p> <p>Output Section : 12 LED indicator</p> <p>Display : 7 Segment (2 Nos.)</p> <p>Scope of Learning</p> <ul style="list-style-type: none"> • Study of Basic Gates • Study of Half Adder using Logic Gates • Study of Full Adder • Study of Half and Full Subtractor using Logic Gates • Study of Magnitude Comparator • Study of Encoder / Priority Encoder • Study of Decoder & Demultiplexers • Study of Multiplexers 	04		

	<ul style="list-style-type: none"> • Study of BCD to 7 Segment Display • Study of Flip-Flop • Study of Register Study of Counter			
09	Study of Multiplexer & Demultiplexer Technical Specification: Operating power supply : +5V DC, 200mA Logic Circuit : Multiplexer & Demultiplexer Logic ICs : IC 74153 and 74154 Experiments : Study of Multiplexer Logic Circuit Study of Demultiplexer Logic Circuit Interconnections : 2 mm sockets Input Logic : Switches (High / Low) Output Logic : LEDs (On/Off) Study points : Test points to study signals at various stages of circuit Other features: on-board Schematic diagram Flexibility of making circuit connections Manual : Operating manual and Experiments along with detailed theory	04		

10	<p>Sciencetech 2612A Advanced Analog CircuitsDevelopment Platform</p> <p>It should have following features: Advanced Analog Lab comprises of following blocks :</p> <ul style="list-style-type: none"> • DC Power Supplies • Function GeneratorToggle Switches • Voltmeter • PC Interface • AC Voltage • Continuity Tester • Potentiometers • Ammeter • Frequency Measurement <p>It should have following Technical Specification: Size of Breadboard : 172.5 mm x 128.5mm Tie Points on Breadboard : 1685 nos (solderless)DC Power Supplies : +5V, 1A (fixed) +12V, 500 mA (fixed) -12V, 500 mA (fixed) +12V, 500 mA (variable) -12V, 500 mA (variable) AC Supply : 9V-0V-9V, 500mA Function Generator : Sine, Square, and Triangularfunctions Frequency range:1Hz to 100KHz In 5steps (variable inbetween the steps) Voltage/Current/Frequency : Voltage range: +12V to -12V (DC) Measurement Current range: 0 to 500 mA (DC) Frequency range: DC to 100KHz (all with respect toground) PC Interface : Acquisition from two analog inputchannels (max. input 1V) Continuity Tester : For testing the continuity (provided with beeper sound)Mains Supply : 110-220V \pm10%, 50Hz</p> <p>Study of:</p> <ul style="list-style-type: none"> • Band Pass Filter • CE configuration of NPN transistor • CB configuration of NPN transistor • CE Amplifier Circuit • Monostable Multivibrator using Transistor • Bistable Multivibrator using Transistor • Astable Multivibrator using Transistor • Diodes in DC circuits • Light emitting diodes in DC circuits • Half wave rectifier • Full wave rectifier • Zener diode as a voltage regulator • Transistor series voltage regulator • Transistor shunt voltage regulator 	04		
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Department Of Electronics & Telecommunication
COEP Technological University, Pune