

Department of Electronics and Telecommunication Engineering

COEP Technological University

(A Unitary Public University of Government of Maharashtra)

Time Table

Class : F. Y. M. Tech Embedded Systems & Computing

With Effect From : 10.02.2025

Academic Year : 2024 - 2025

Term : II (Second)

Day TIME	9.30 am To 10.30 am	10.30 am To 11.30 am	11.30 am To 12.30 pm	12.30 pm To 1.30 pm	1.30 pm To 2.30 pm	2.30 pm To 3.30 pm	3.30 pm To 4.30 pm	4.30 pm To 5.30 pm	5.30 pm To 6.30 pm
MON		CC (PPT) ETSH - 202	CC (PPT) ETSH - 202	ESS Lab (SPMe) PG Lab - 1					
TUE		EOS (PPB) ETSH - 202	ESS (SPMe) ETSH - 202	ESS (SPMe) ETSH - 202	CC (PPT) ETSH - 202				
WED	IOC ()	EOS (PPB) ETSH - 202	ESS (SPMe) ETSH - 202		WSN (RDJ) ETSH - 202	WSN (RDJ) ETSH - 202			
THU	IOC ()	EOS Lab (PPB) PG Lab - 1			DAEC (YMV) ETSH - 202	DAEC (YMV) ETSH - 202			
FRI	IOC ()	EOS (PPB) ETSH - 202	WSN (RDJ) ETSH - 202		DAEC (YMV) ETSH - 202	DAEC Lab (YMV) PG Lab - 1			
Sat									

Embedded Operating system (EOS) (Th + Lab): Dr. P. P. Bartakke, **Embedded System Security (ESS)(Th+Lab):** Dr. (Ms.) S. P. Metkar

Data analytics on Edge Computing (DAEC)(Th + Lab): Dr. (Mrs.) Y. M. Vaidya,

Program Specific Elective - II: Cloud Computing (CC)(Th): Dr. P. P. Tasgaonkar

Program Specific Elective - III: Wireless Sensor Networks (Th): Dr. (Mrs.) R. D. Joshi

_In-charge, Timetable Committee,
Department of Electronics & Telecommunication Engineering