

COEP Technological University

Dept of CSE(Draft Timetable v4 ,Odd-Sem 24-25, Effective 12 Aug 24)

Timetable for Class: SY Computer Engineering Div 1

Color Codes: **Batch** **Room** **Class** **Subject**

	08:30	09:30	10:30	11:30	12:30	13:30	14:30	15:30	16:30	17:30	18:30
M on		OE		PPL AC-203	DSA AC-203	LUNCH	SY1-S1 DSL MT-Lab				
							SY1-S2 FOSS2 DSALab				
Tu e		OE	PPL AC-203	MT AC-203	DSA AC-203	LUNCH	SY1-S2 DSL MT-Lab				
							SY1-S3 FOSS2 DSALab				
W ed				MT AC-203	DSA AC-203	LUNCH	SY1-S3 DSL MT-Lab		External Lectures Slot	External Lectures Slot	
							SY1-S4 FOSS2 DSALab				
Th u				MT AC-203	Env AC-203	LUNCH	SY1-S4 DSL MT-Lab		IL AC-203	IL AC-203	
							SY1-S5 FOSS2 DSALab				
Fri				PPL AC-203		LUNCH	SY1-S5 DSL MT-Lab		Entre AC-203	Entre AC-203	
							SY1-S1 FOSS2 DSALab				

Legend Page

Room Short Name	Room Name
AC-203	Academic Complex - 203 (2nd Floor)
DSL	Digital Systems Lab (I Floor, CSE Dept)
FOSS2	FOSS Lab, Column-2 (3rd Floor,Entc Extension)

Subject Short Name: Batch Names	Teacher Name
Entre	Applied Science Teacher3
IL	Indian Language Teacher2
PPL	Kalyani Vidhate
MT	Pravin Revankar
Env	Yogeshwari Sonawane(Applied Science)
MT-Lab: SY1-S4, SY1-S1, SY1-S2, SY1-S3	Pravin Revankar
MT-Lab: SY1-S5	Sneha Palimkar
DSALab: SY1-S5	Abhiraj Ubale (Adjunct)
DSALab: SY1-S4, SY1-S3	Siddharth Gaikwad
DSALab: SY1-S2, SY1-S1	Jibi Abraham
DSA	Shreedha Kalamkar

Subject Short Name	Subject Name
Entre	Entrepreneurship
IL	Indian language
PPL	Principles of Programming Languages
MT	Microprocessor Techniques
Env	Environmental Studies
MT-Lab	Microprocessor Techniques Laboratory
DSALab	Data Structures and Algorithms Laboratory
DSA	Data Structures and Algorithms

Timetable Incharge

Head of Department